

35474 U.S. PTO  
10/065717  
11/13/02

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

501164

APPL NUM 10065717	FILING DATE 11/13/2002	CLASS 438	SUBCLASS 300	GAU 4765	EXAMINER
<b>**APPLICANTS:</b> Yang Ming-Sheng, Kao Chia, Chien Chin Cheng.					
<b>**CONTINUING DATA VERIFIED:</b> This application is a CIP of 09/683,691 02/04/2002					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO.	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		NAUP0504USA	
Verified and Acknowledged Examiner's initials					
TITLE : Method of preventing leakage current of a metal oxide semiconductor transistor					

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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